DAILY ASSESSMENT REPORT

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| **Date:** | **12-06-20** | **Name:** | **Rachana C Hulikatti** |
| **Course:** | **PCB design using Kicad** | **USN:** | **4AL17EC108** |
|  |  | **Semester & Section:** | **6th B** |

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| **FORENOON SESSION DETAILS** |
| **Image of session** |

**Report**

# Peel-off Mask Design Rule Specifications:

# Clipping Rules:

Legend clipping clearance is 0.100mm (4mil) back from the Soldermask openings.

Any parts of line smaller than 0.100mm (4mil) will be removed.

# Board Outline:

Please always include the board outline in your legend layers output data as this helps avoid any potential misalignment, mirroring or rotation issues.

This is best done using a small line – e.g. 0.500mm (20mil) wide – where the center of the line is the exact board outline.

We will remove this line in the actual production data we use for manufacturing your boards.

In all cases we will clip away any legend text within 0.200mm (10mil) of the board edge.